

FIG. 1

FIG.2

ADDRESS[7:0]		DATA[63:0]	
11	00-CF	PACKET DATA	12
13	D0	PORT 0 TRANSMIT DESCRIPTOR	14
	D1	PORT 1 TRANSMIT DESCRIPTOR	
	D2	PORT 2 TRANSMIT DESCRIPTOR	
	D3	PORT 3 TRANSMIT DESCRIPTOR	
	D4	PORT 4 TRANSMIT DESCRIPTOR	100
	D5	PORT 5 TRANSMIT DESCRIPTOR	
	D6	PORT 6 TRANSMIT DESCRIPTOR	
	D7	PORT 7 TRANSMIT DESCRIPTOR	
15	D8	PORT 8 TRANSMIT DESCRIPTOR	16
	D9-DF	UNUSED	
17	E0-FF	32 ARL TABLE ENTRIES	18

FIG.3

ADDRESS[16:0]		DATA[63:0]	
19	0000-00FF	MEMORY BLOCK 0	20
	0000-01FF	MEMORY BLOCK 1	
	0000-02FF	MEMORY BLOCK 2	
	0000-FEFF	MEMORY BLOCK 3 TO 2046	
	0000-FFFF	MEMORY BLOCK 2047	
	0000-1FFFF	OPTIONAL MEMORY BLOCK 2048-4095	

FIG. 4

ADDRESS[7:0]		DATA[63:0]									
		[63:56]	[55:48]	[47:40]	[39:32]	[31:24]	[23:16]	[15:8]	[7:0]		
00		B0	B1	B2	B3	B4	B5	B6	B7		
01		B8	B9	B10	B11	B12	B13	B14	B15		
-		-	-	-	-	-	-	-	-		
07		B56	B57	B58	B59	B60	B61	B62	B63		
08		B64	B65	UNUSED							
08-CF		UNUSED									

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FIG.5

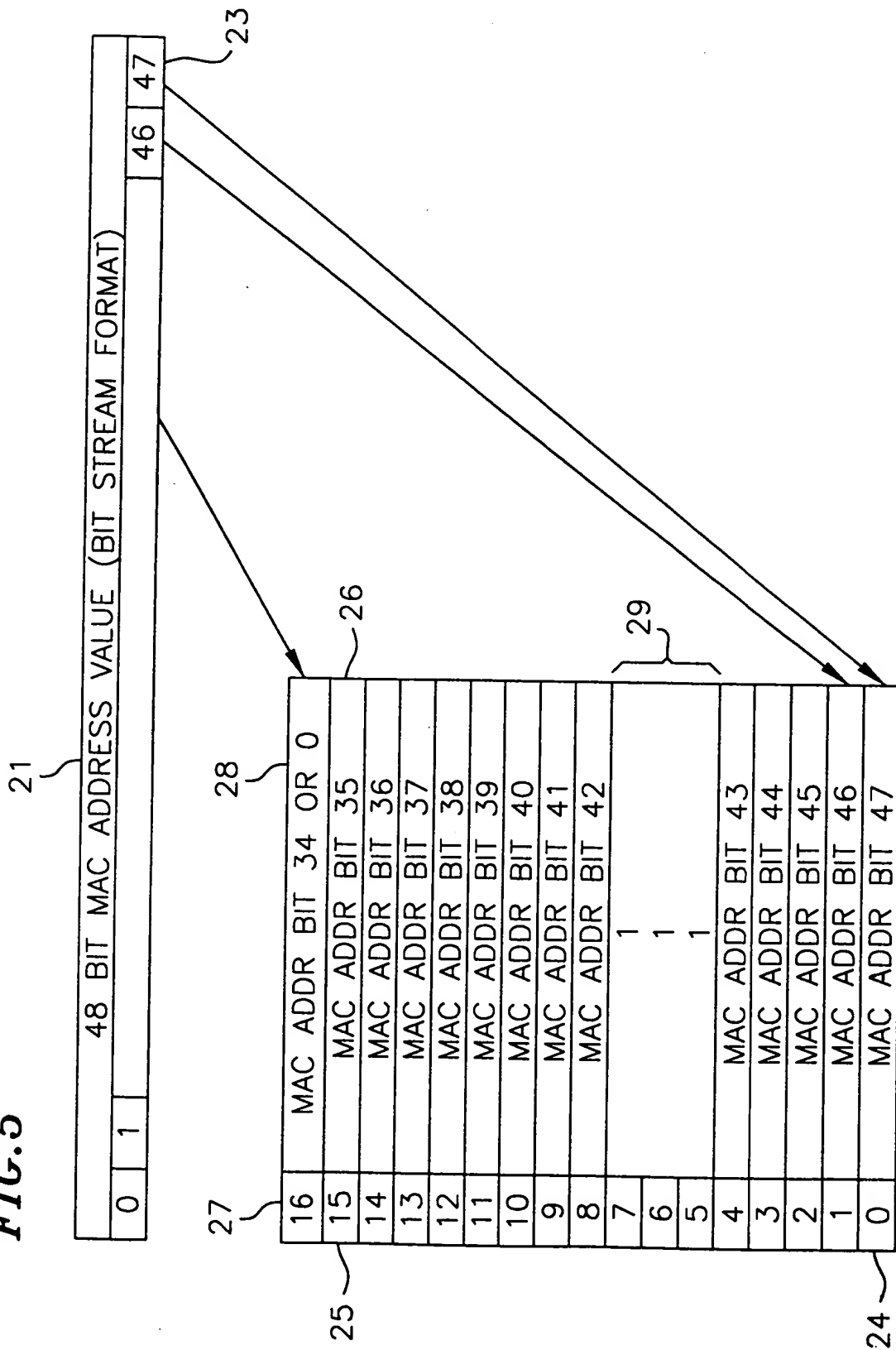
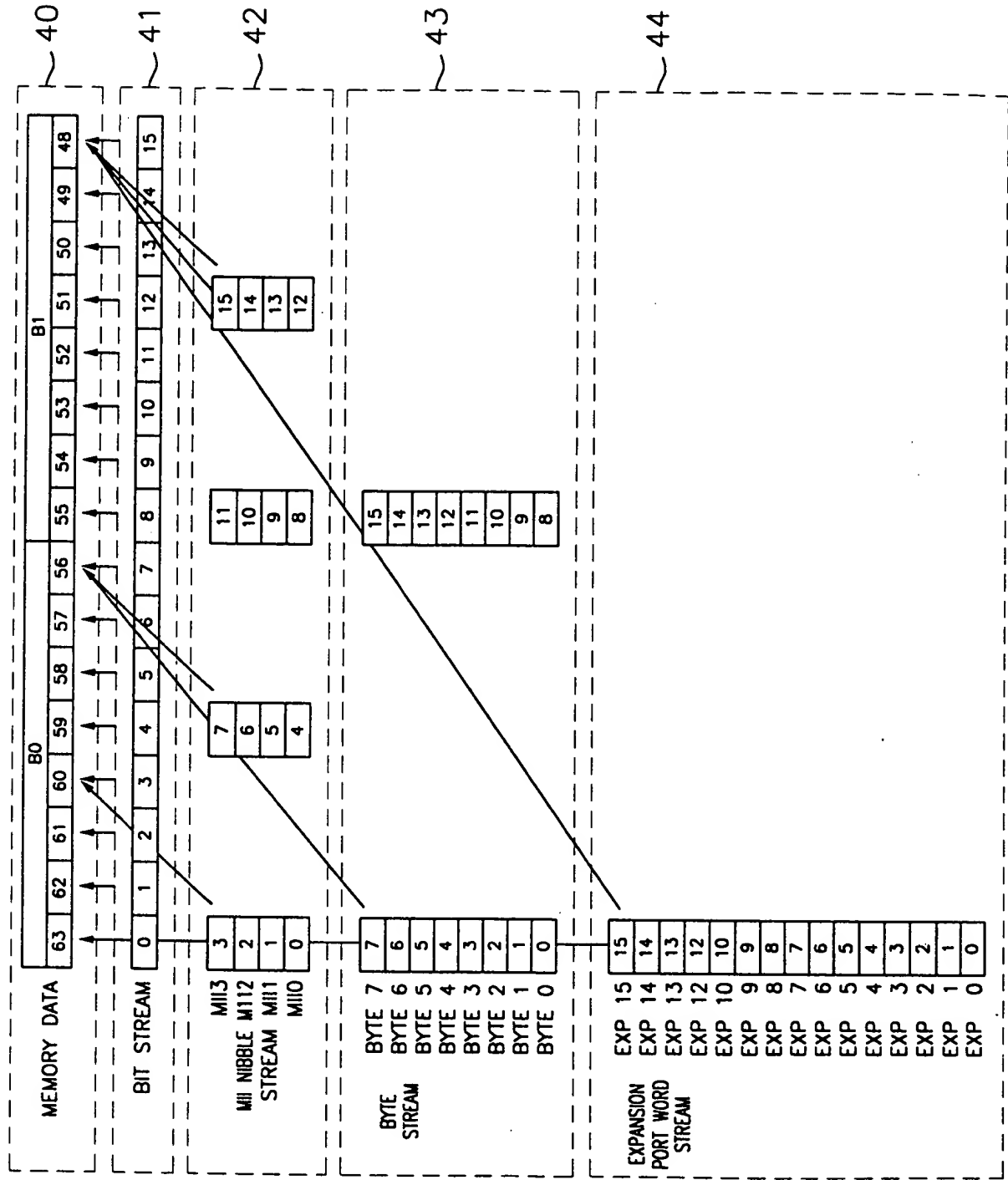


FIG. 6



00000000000000000000000000000000

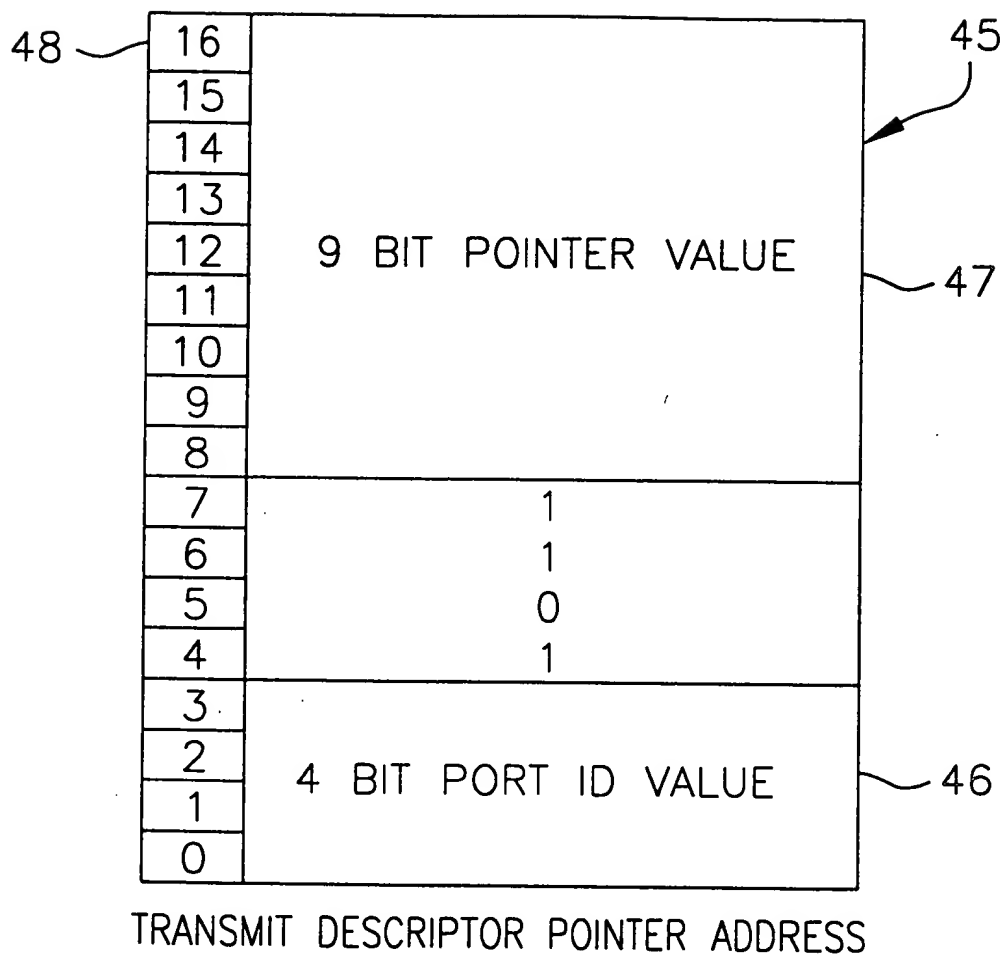
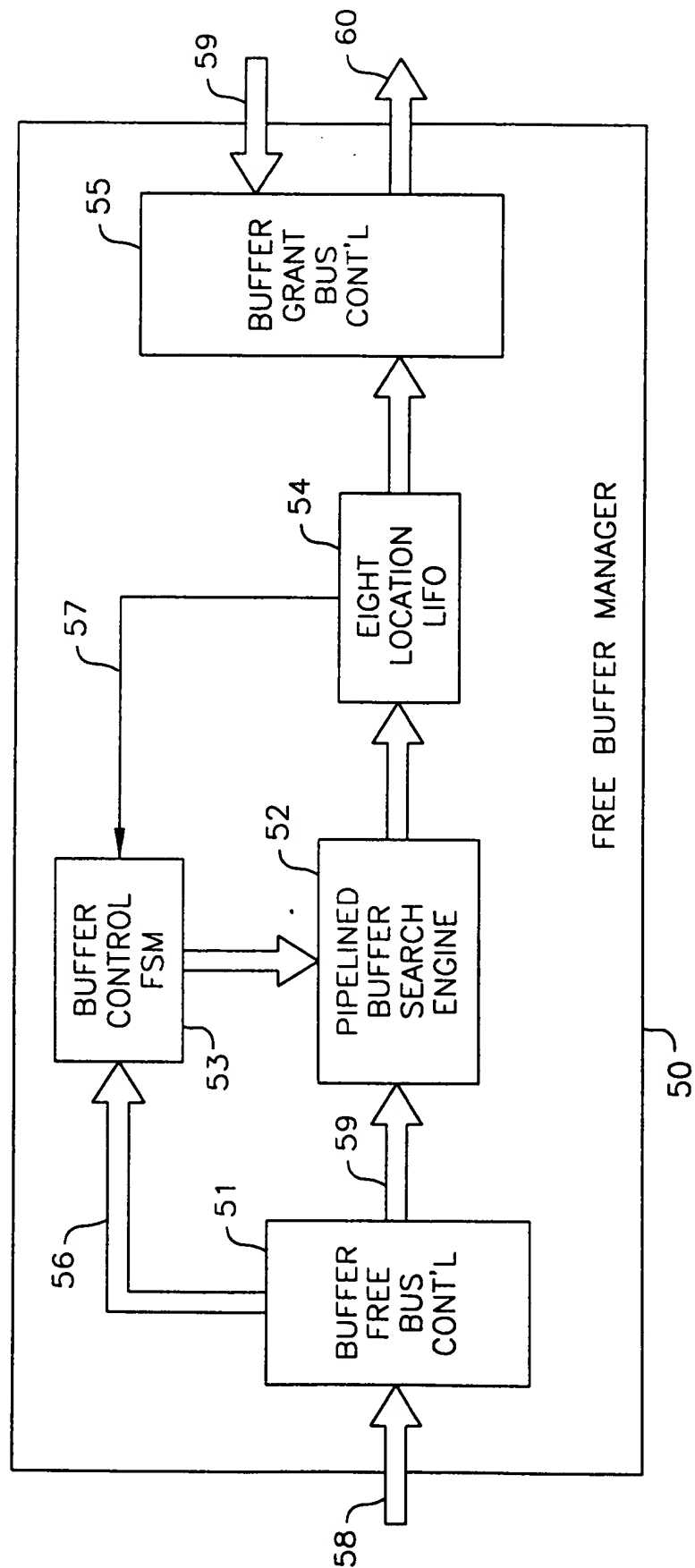


FIG. 7

FIG. 8



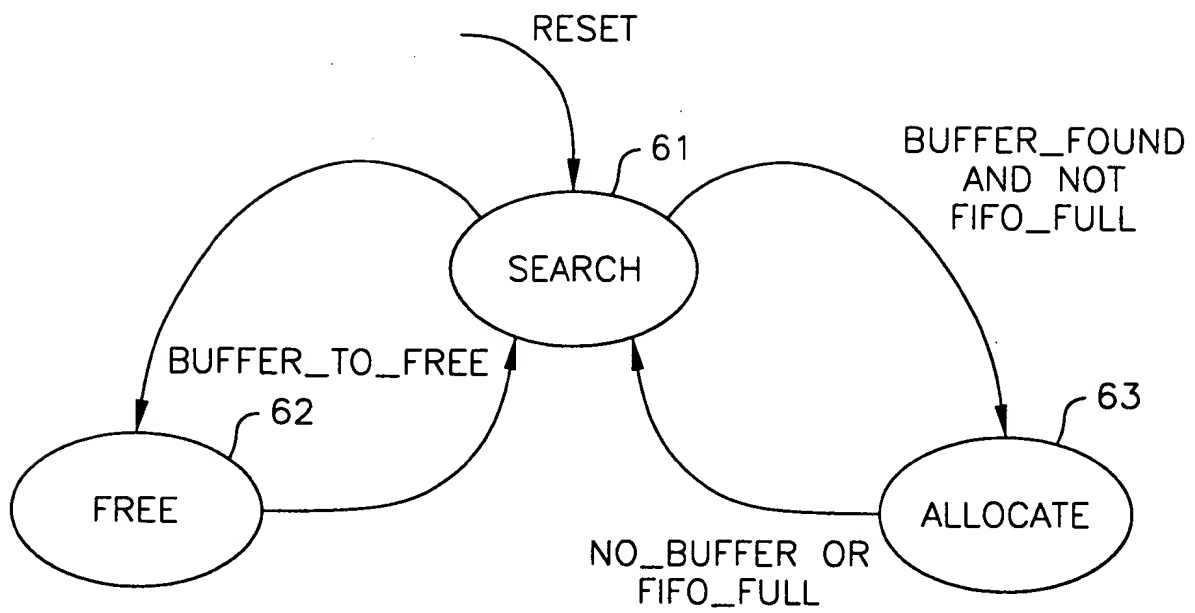


FIG. 9

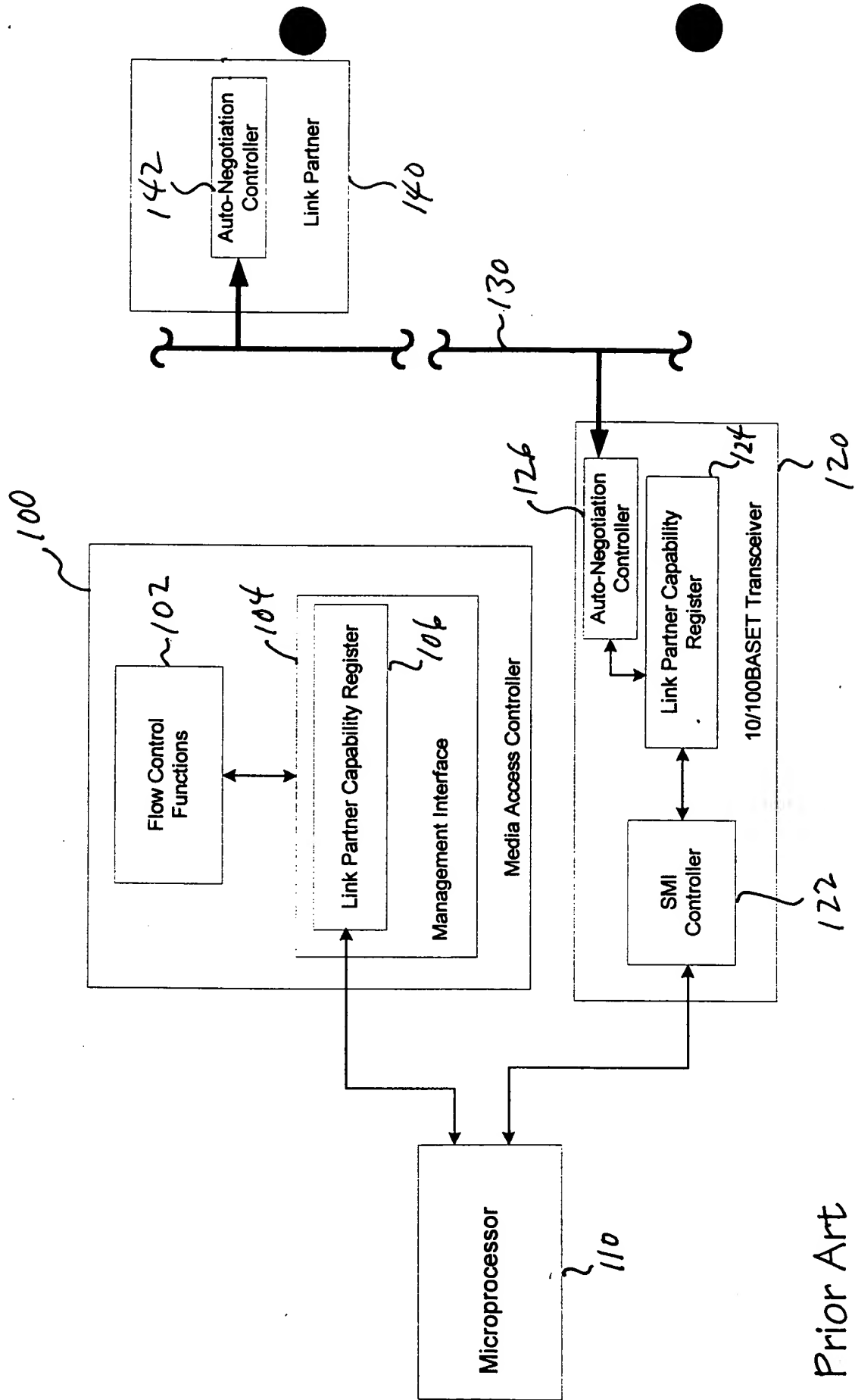
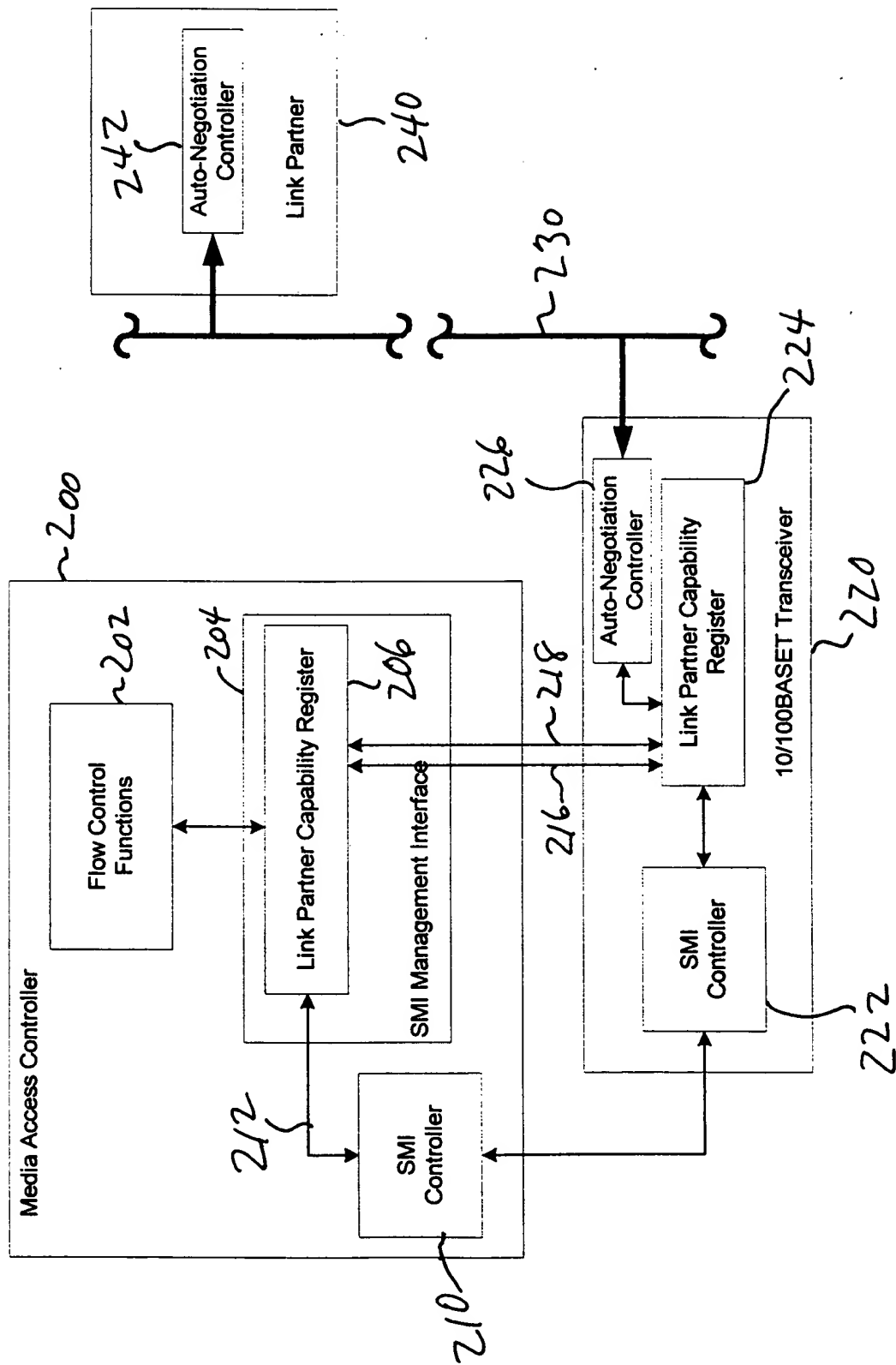


Figure 10

Prior Art



Prior Art

Figure 11

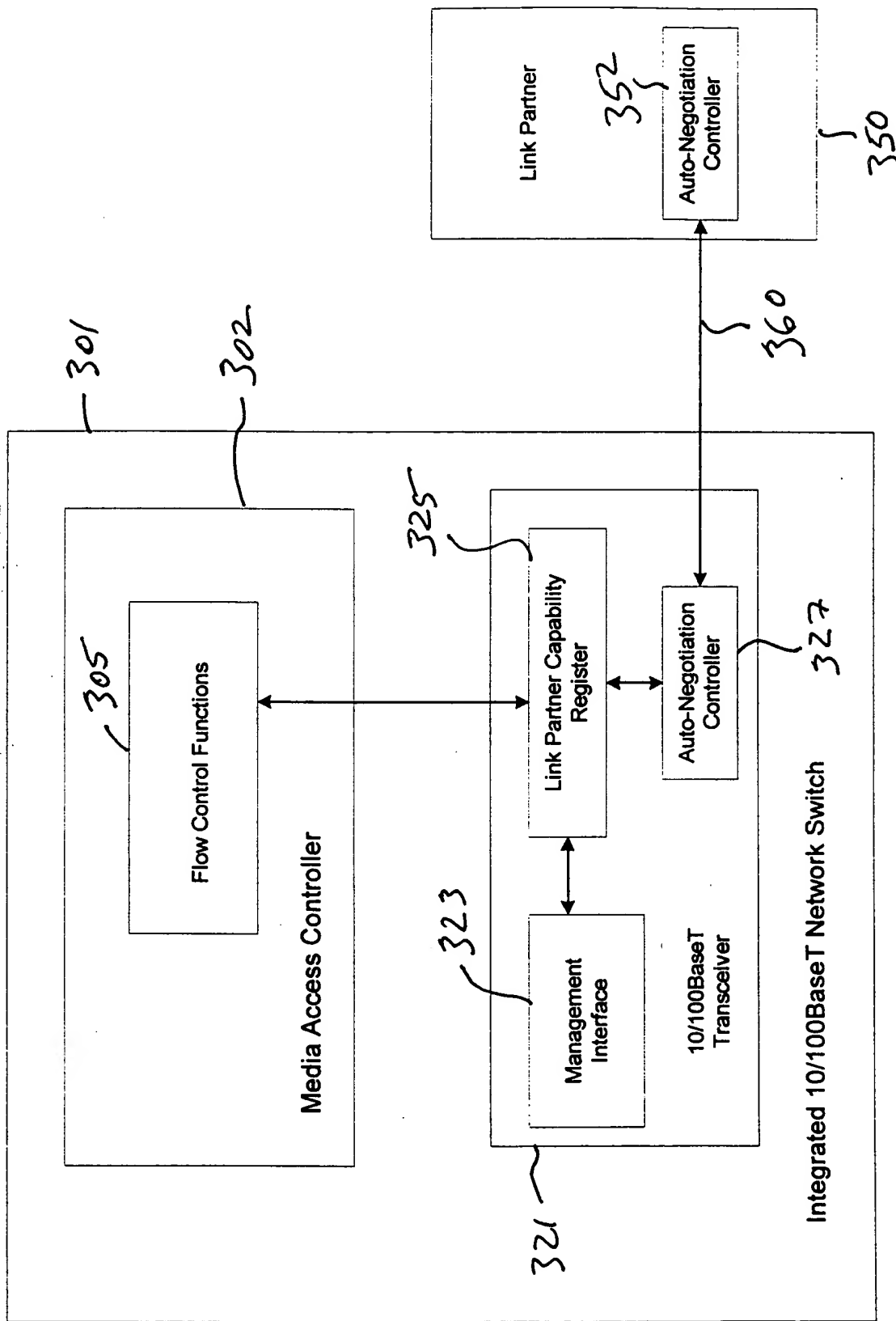


Figure 12

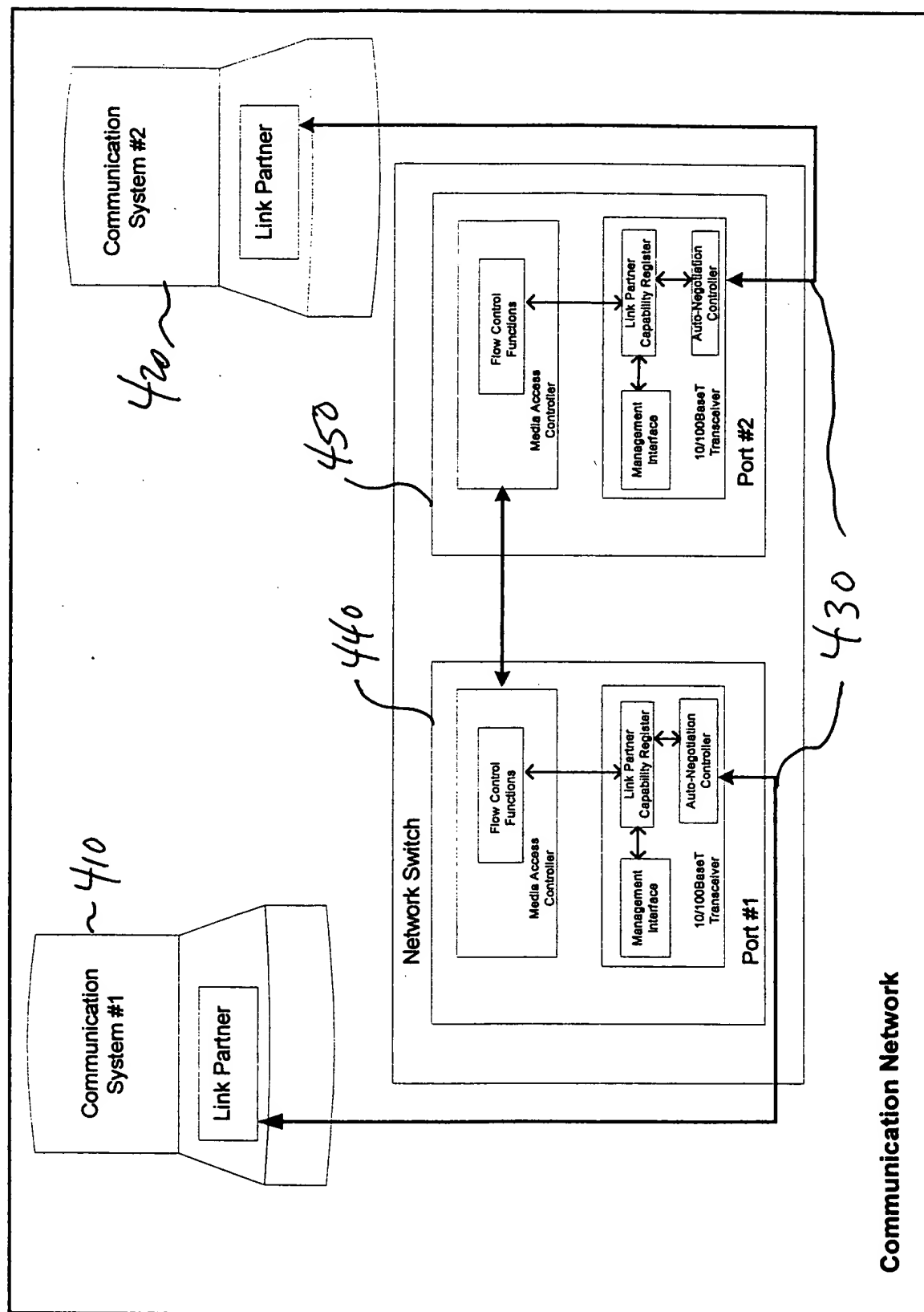


Figure 13